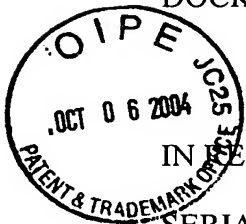


DOCKET NO.: 219030US2S/hyc



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Toshitake YAEGASHI

SERIAL NUMBER: 10/058,343

GROUP: 2814

FILED: January 30, 2002

EXAMINER: Marcos D. PIZARRO
CRESPO

FOR: NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE IN WHICH
SELECTION GATE TRANSISTORS AND MEMORY CELLS HAVE DIFFERENT
STRUCTURES

REQUEST TO CORRECT TITLE OF INVENTION

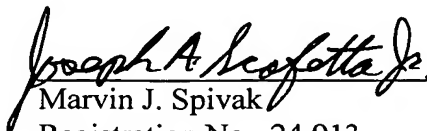
MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

SIR:

In the matter of the above-identified application for patent, we hereby request
correction of your records to reflect the correct title of the invention. The title of the
invention should read as follows: NON-VOLATILE SEMICONDUCTOR MEMORY
DEVICE IN WHICH SELECTION GATE TRANSISTORS AND MEMORY CELLS
HAVE DIFFERENT STRUCTURES.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.


Marvin J. Spivak

Registration No. 24,913
Joseph A. Scafetta, Jr.
Registration No. 26,803

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 05/04)